

INFORMAL

100

Current Mirror Circuit
110

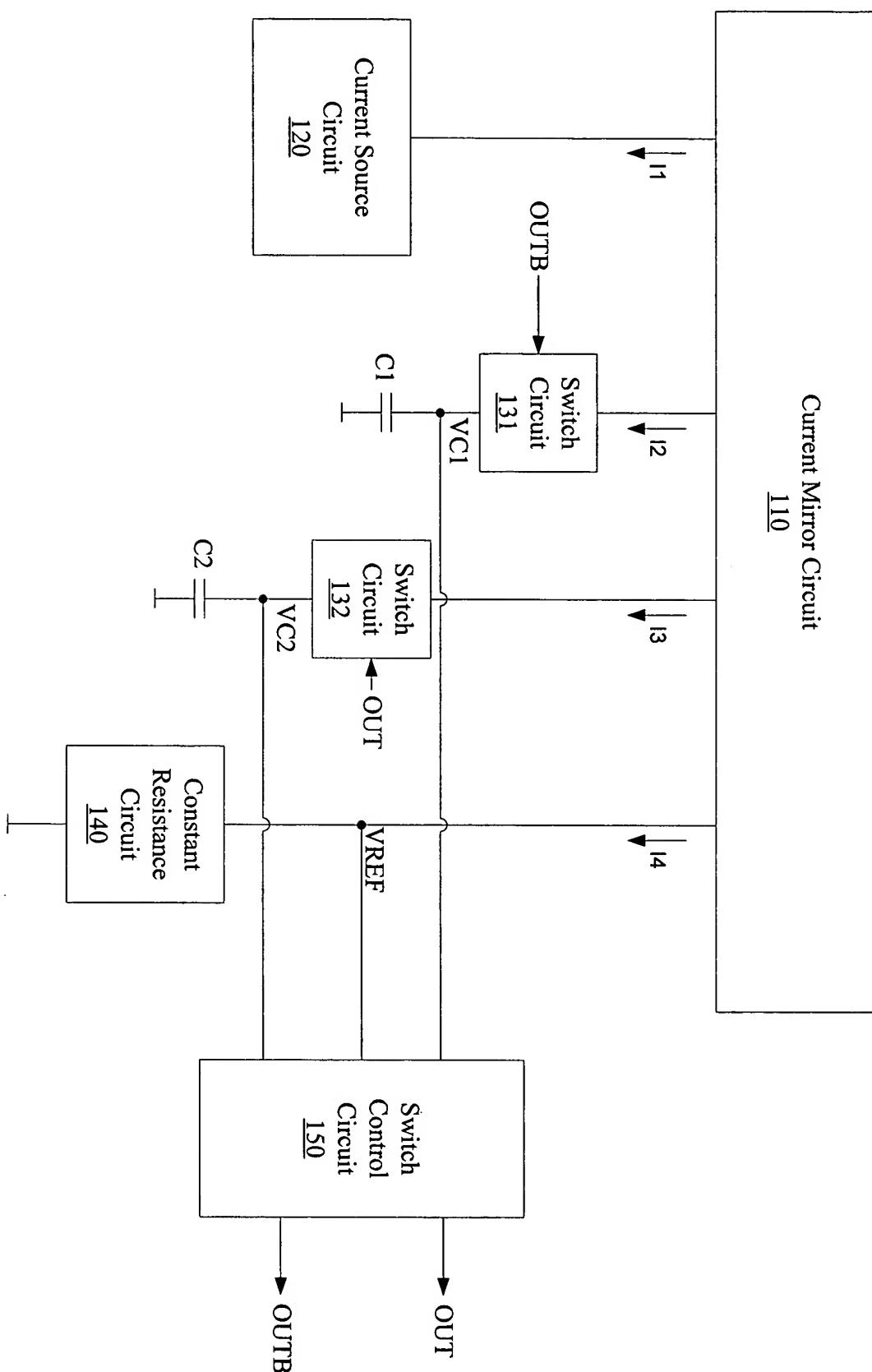


FIGURE 1

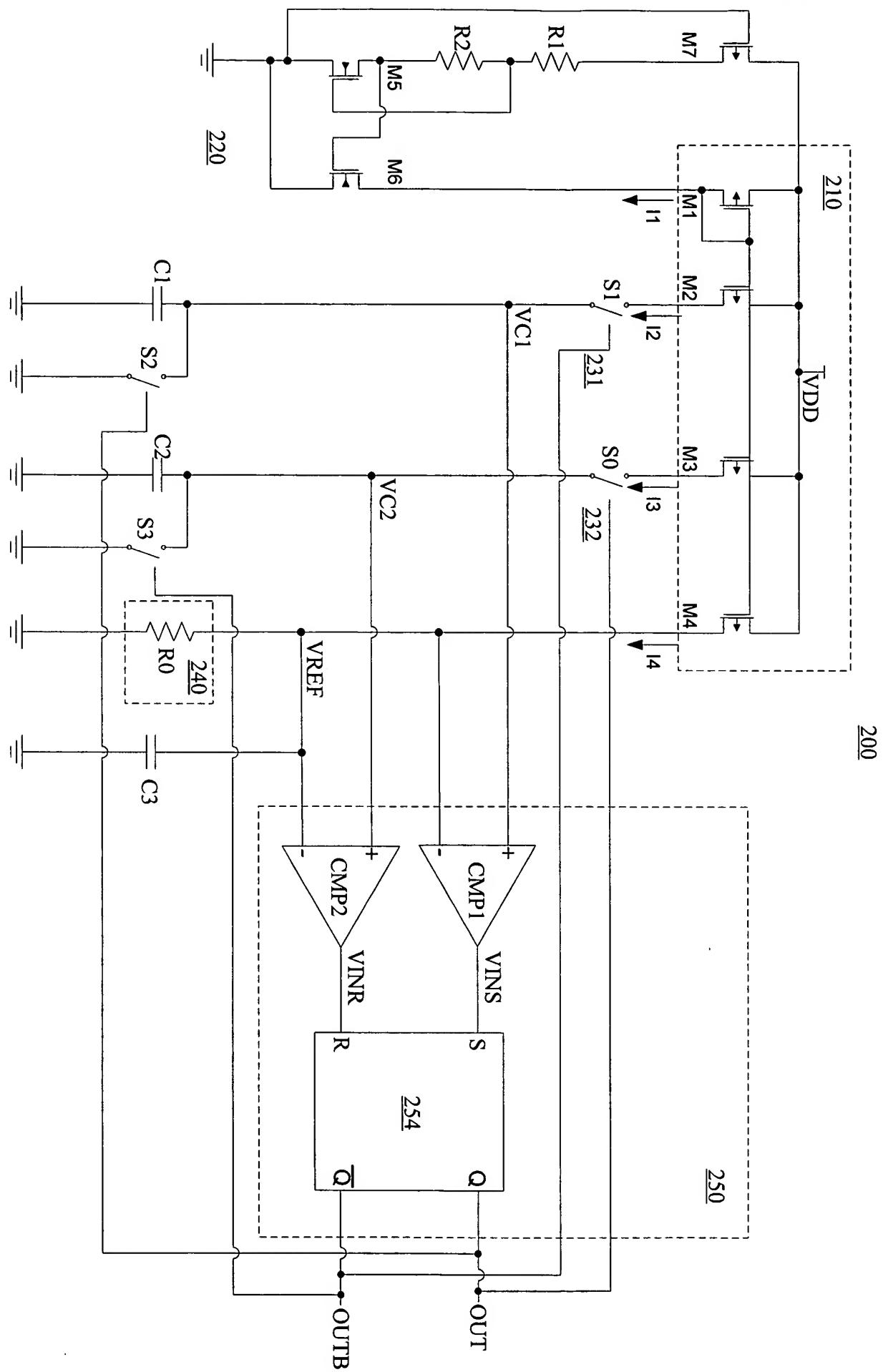
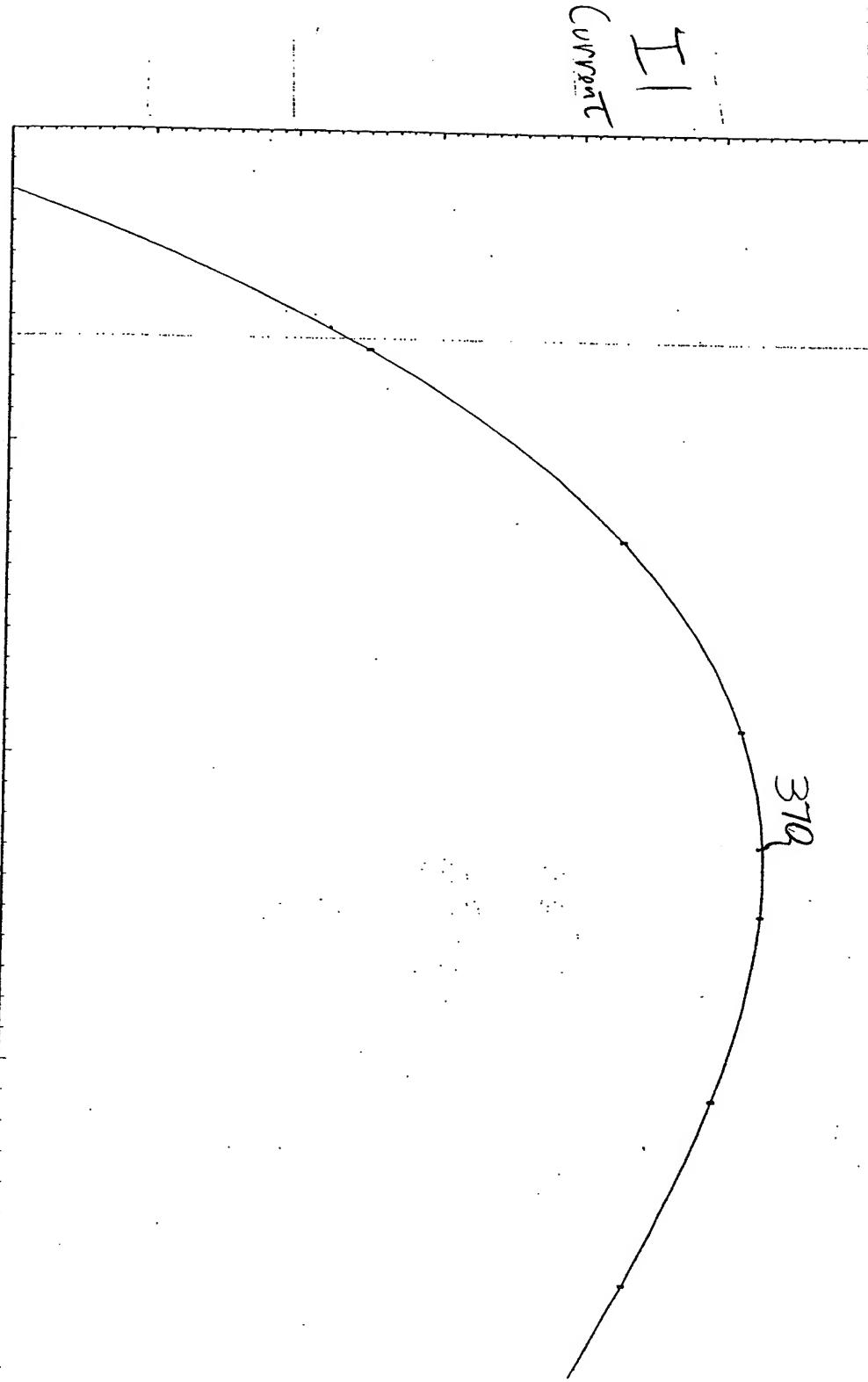


FIGURE 2

(V) VDP

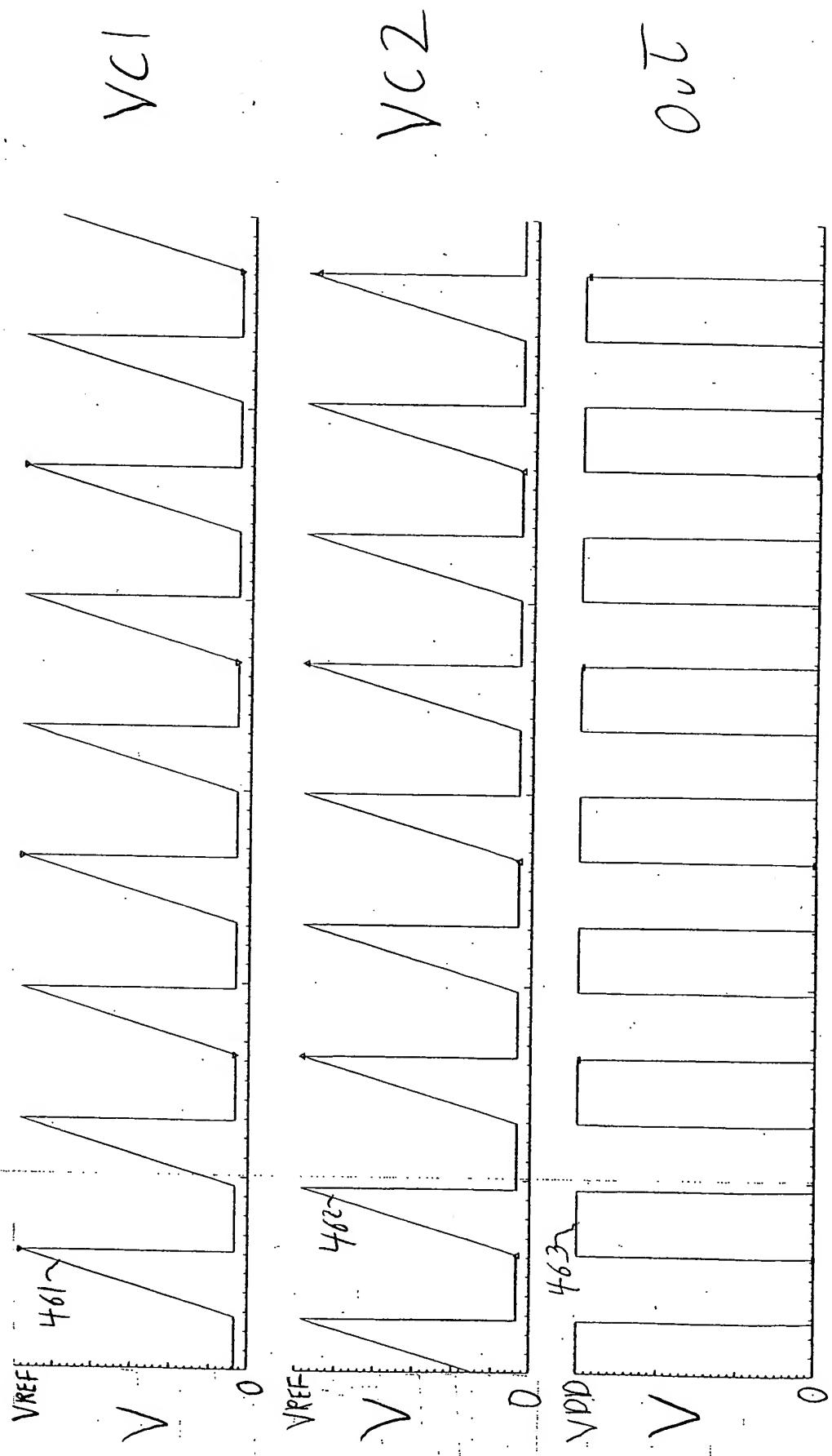
FIGURE 3



INFORMAL

FIGURE 4

time



NORMAL